<u>Tentative Schedule</u>

High End Workshop on Image Processing and its Applications using VLSI Architectures

(Under the KARYASHALA Scheme – A SERB initiative)

3rd – 8th July 2023, DA-IICT Gandhinagar, Gujarat, India

Date	08:30 am to 09:00 am	09:00 am to 09:45 am	09:45 am to 10:15 am	Slot-1 10:15 am to 11:30 am	11:30 am to 11:45 am	Slot-2 11:45 am to 1:00 pm	01:00 pm to 2:00 pm	Slot-3 2:00 pm to 5 pm
03rd July 2023 (Monday)	Registration (AIP Lab, Lab Building, Ground Floor)	Inauguration (AIP Lab, Lab Building, Ground Floor)	High Tea	What Is Image Processing: Overview, Applications, Benefits and More (Dr. Maheshkumar H Kolekar IIT Patna)	Tea & Snacks	Image Processing Toolbox in MATLAB (Dr. Manish Khare, DA-IICT Gandhinagar)	Lunch	Image Processing Lab in MATLAB (Dr. Maheshkumar H Kolekar and Dr. Manish Khare)

Date	Slot-1 10:00 am to 11:15 am	11:15 am to 11:30 am	Slot-2 11:30 am to 12:45 pm	12:45 pm to 2:00 pm	Slot-3 2:00 pm to 3:30 pm	03:30 pm to 03:45 pm	Slot-4 03:45 pm to 05:00 pm
04th July 2023 (Tuesday)	Modeling, Simulation and Hardware (Dr. Yash Agrawal, DA-IICT Gandhinagar)	Tea & Snacks	Introduction to Digital Programming (Dr. Yash Agrawal, DA-IICT Gandhinagar)	Lunch	Lab on Digital Programming (Dr. Yash Agrawal, DA-IICT Gandhinagar)	Tea & Snacks	Lab on Image Processing using FPGA (Dr. Yash and Dr. Manish, DA-IICT Gandhinagar)
05th July 2023 (Wednesday)	Parallel and Pipelined Architecture for Image Processing Applications (Dr. Debasish Mukherjee, Univ. of Illinois at Singapore Pte Ltd)		VLSI Architectures for Computational Intensive Computer Vision algorithms (Dr. Anirban Chakraborty, University of Engineering & Management, Kolkata)		Lab on VLSI Architecture and FPGA (Dr. Anirban Chakraborty, University of Engineering & Management, Kolkata)		Lab on VLSI Architecture and FPGA (Dr. Anirban Chakraborty, University of Engineering & Management, Kolkata)
06th July 2023 (Thursday)	Signal Processing: Algorithms to VLSI Architecture Perspective (Dr. Kishor P. Sarawadekar, IIT (BHU) Varanasi)		Signal Processing: Algorithms to VLSI Architecture Perspective (Dr. Kishor P. Sarawadekar, IIT (BHU) Varanasi)		Lab on Signal Processing Architectures and HDL Coding (Dr. Kishor P. Sarawadekar, IIT (BHU) Varanasi)		Lab on Signal Processing Architectures and HDL Coding (Dr. Kishor P. Sarawadekar, IIT (BHU) Varanasi)
07th July 2023 (Friday)	Image Processing Application in Video Surveillance (Dr. Alok Kumar Singh Kushwaha, Guru Ghasidas Vishwavidyalaya, Bilaspur)		Color Image Processing (Dr. Alok Kr Singh Kushwaha, Guru Ghasidas Vishwavidyalaya, Bilaspur)		Lab on Video Surveillance (Dr. Alok Kr Singh Kushwaha, Guru Ghasidas Vishwavidyalaya, Bilaspur)		Lab on Color Image Processing (Dr. Manish Khare, DA-IICT Gandhinagar)
08th July 2023 (Saturday)	Image Processing Techniques in Satellite Images/Remote Sensing (Dr. A M Ramiya, Indian Institute of Space Science and Technology, Thiruvananthapuram)		Quiz and Award		Valedictory		